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U.S. PATENT AND TRADEMARK OFFICE
AMENDMENT TRANSMITTAL FORM

TI-30482

Attorney Docket No.

In re the Application of

Gary L. Swoboda

Serial No: 09/943,595

Filed: August 30, 2001

For: Collecting and Exporting On-Chip Data
Processor Trace and Timing Information With
Differing Collection and Export Formats

Conf. No.: 2423

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below:

CLAIMS AS AMENDED						
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
Total Claims	34	Minus	29	= 5	x \$50 =	\$ 250.00
Independent Claims	3	Minus	3	= 0	x \$200 =	\$ 00.00
TOTAL ADDITIONAL FEE FOR THIS AMOUNT						\$ 250.00

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Robert D. Marshall, Jr.
Attorney for Applicant
Reg. No. 28,527

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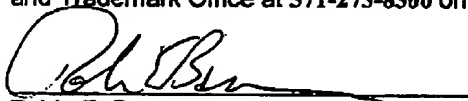
From: Robert D. Marshall, Jr.
Texas Instruments Incorporated
Facsimile: 972-917-4418
Phone: 972-917-5290

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of TI-30482
Gary L. Swoboda Art Unit: 2128
Serial No.: 09/943,595 Examiner: Akash Saxena
Filed: August 30, 2001 Conf. No.: 2423
For: Collecting and Exporting On-Chip Data Processor Trace and Timing Information With
Differing Collection and Export Formats

CERTIFICATION OF FACSIMILE TRANSMISSION

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November 10, 2005
Date

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<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT <u>116 (22 Pages)</u>
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT <u>(# Page)</u>
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
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<input type="checkbox"/> DIVISIONAL APPN	
NAME OF INVENTOR(S): Gary L. Swoboda	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/943,595 Filing Date: August 30, 2001	
TITLE OF INVENTION: Collecting and Exporting On-Chip Data Processor Trace and Timing Information With Differing Collection and Export Formats	
TI FILE NO.: TI-30482	DEPOSIT ACCT. NO.: 20-0668
FAXED: 11/10/05 DUE: 12/8/05 ATTY/SECY: RDM/rcb	

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RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2128

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swoboda
Serial No.: 09/943,595
Filed: August 39, 2001

Art Unit: 2128
Examiner: Akash Saxena
Docket: TI-30482

For: COLLECTING AND EXPORTING ON-CHIP DATA PROCESSOR TRACE AND
TIMING INFORMATION WITH DIFFERING COLLECTION AND EXPORT
FORMATS

AMENDMENT UNDER 37 C.F.R. 1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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UNDER 37 C.F.R. §1.6(b)

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Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response
to the Examiner's Office Action dated September 8, 2005. These
amendments are respectfully submitted as a full and complete
response to that Action.

Please amend the above referenced application as follows:

11/14/2005 LWONDIM1 00000023 200668 09943395

01 FC:1202 250.00 DA

- 1 -